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AMENDMENT TO THE CLAIMS

1. (Canceled)

2. (Currently amended) An image processor comprising:

an imager for capturing an image of an object and outputting image data representing the image captured;

a compressor/expander, which receives and compresses the image data and then outputs the compressed image data or which receives and expands the compressed image data and then outputs the expanded image data;

an image memory for storing the compressed image data thereon;

a display memory for storing the expanded image data thereon;

a display for presenting thereon the expanded image data that has been once stored on the display memory; and

an interface for recording the compressed image data, which has been once stored on the image memory, on a storage medium,

wherein image data corresponding to a series of images which are captured consecutively by the imager includes an image which is transferred from the image memory to the storage medium while the [[series of images]] image is presented by the display.

3. (Previously presented) The processor of claim 2, wherein the compressor/expander produces a reduced-size image for each said image captured and compresses the reduced-size image to obtain and output the compressed image data, and

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wherein the compressor/expander expands the compressed image data, representing the series of images, and then outputs the expanded image data to the display memory so that the reduced-size versions of the series of images can be added one by one on the same display in the order in which the images have been captured so as to present a plurality of images on the display.

4. (Original) The processor of claim 2, wherein the compressor/expander expands the compressed image data, representing each of the series of images which is being transferred to the storage medium, and then outputs the expanded image data to the display memory so that each said image being transferred can be presented on the display.

5. (Canceled)

6. (Previously presented) The image processor of claim 2, wherein the display presents the series of images while the compressed image data corresponding to the series of images is stored on the storage medium.

7. (Previously presented) The image processor of claim 2, wherein the image memory and the display memory are implemented as a single memory.

8. (Currently amended) A method comprising the steps of:
    successively receiving image data corresponding to a series of images captured
consecutively by an imager;

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successively compressing the received image data as compressed image data by a compressor/expander;

temporarily storing the compressed image data on an image memory;

successively outputting the compressed image data to the compressor/expander;

successively expanding the compressed image data by the compressor/expander;

successively storing the image data expanded by the compressor/expander on a display memory; and

storing an image of the compressed image data [[successively]] on a storage medium while the [[series of images]] image is presented [[on]] by a display based on the image data stored on the display memory.

9. (Previously presented) The method of claim 8, wherein the image memory and the display memory are implemented as a single memory.